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IMAGE DISPLAYING METHOD, GAME SYSTEM AND COMPUTER READABLE STORAGE MEDIUM STORING IMAGE DISPLAYING PROGRAM

BACKGROUND OF THE INVENTION

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5 The present invention relates to an image displaying method in a game system and the like, and more particularly to a technical field of an image displaying method for constituting an image by processing image data for each frame and outputting the frames to a display device.

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10 Recently, the quality of a game picture has been improved with the improvement of the processing performance of a game machine. In the conventional home use game system, the game picture is constituted by outputting image data to be displayed on the monitor of the NTSC television system by each frame. In such a game system, 60 frames of frame image data are generated in one second corresponding to an NTSC interlace scanning.

15 It is desirable to improve a resolution by increasing pixel number constituting frame image data for the improvement of the game picture. On the other hand, it is necessary to control the total number of pixels of the frame image data because there is certain amount of restriction on memory size for storing

frame image data and image processing capacity. Therefor, there exists a conventional method to substantially double the resolution by, for example, shifting a position of an even number frame from that of an odd number frame in vertical direction by half size of one pixel and outputting frame image data by turns. Thus, it is possible to display a high quality game picture while controlling the data size and processing amount in image processing.

However, even when the above described method is applied, the image processing must be conducted 60 times per one second, resulting in a seriously heavy load of processing. On the other hand, there will be a heavy load when other processing, for example, a calculation of three-dimensional expression is conducted in synchronism with image processing of each frame. In some situations of the game, it is possible that all of the processing can not be finished within a frame. As a result, the synchronism for each frame can not be attained and a disorder appears on the game picture.

SUMMARY OF THE INVENTION

The present invention is made to solve these problems. It is an object of the present invention to provide an image display method capable of displaying a

high-quality picture without disorder of the picture, even when the processing load increases.

In order to solve the above problems, according to the first embodiment of the present invention, there is provided an image displaying method for

5 displaying an image by outputting image data by each frame to a display device, wherein it is possible to alternatively set a first display mode which outputs image data to the display device with a different pixel arrangement for each frame and a second display mode which outputs image data to the display device with an identical pixel arrangement for each frame, judging a scale of a processing load
10 performed within one frame, and setting the first display mode when the load is judged to be light or setting the second display mode when the load is judged to be heavy.

The second embodiment of the present invention is a game system displaying an image by outputting image data to a display device for each frame,
15 comprising a display mode setting device for alternatively setting a first display mode outputting image data to the display device with a different pixel arrangement for each frame and a second display mode outputting image data to the display device with an identical pixel arrangement for each frame, wherein the display mode setting device judges a scale of processing load performed within

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one frame, and sets the first display mode when the load is judged to be light, or sets second display mode when the load is judged to be heavy.

The third embodiment of the present invention is a computer readable storage medium storing an image display program formed so as to make a

5 computer, performing an image display processing and displays an image by outputting image data to a display device by each frame, perform the functions of; being able to set alternatively a first display mode which outputs image data to the display device with a different pixel arrangement for each frame and a second display mode which outputs image data to the display device with an identical
10 pixel arrangement for each frame, and judging a processing load performed within one frame and setting the first display mode when the load is judged to be light or setting the second display mode when the load is judged to be heavy.

According to these embodiments, an image is displayed by setting the first display mode when the load is light and by setting the second display mode when
15 the load is heavy during outputting the image data to the display device by each frame. Accordingly, the resolution is improved artificially as a different pixel arrangement for each frame in a normal situation. On the other hand, the processing load is lightened if the processing is too busy to be performed. Therefore, the disorder of picture quality is prevented by arranging identical pixel

for each frame and an image is displayed with a well-balanced adjustment between the quantity of processing and the quality of the picture.

The fourth embodiment of the present invention is an image displaying method wherein the first display mode constitutes image data by arranging pixel data at different pixel positions each other for an odd number frame and an even number frame.

The fifth embodiment of the present invention is a game system wherein the display mode setting device makes the first display mode constitute image data by arranging pixel data at different pixel positions each other for an odd number frame and an even number frame.

The sixth embodiment of the present invention is a computer readable storage medium storing an image displaying program wherein the first display mode constitutes image data by arranging pixel data at different pixel positions each other for an odd number frame and an even number frame.

According to these embodiments, as the processing load is judged by comparing the processing time with the predetermined reference value, judgement for switching the first display mode to the second display mode is performed accurately.

The seventh embodiment of the present invention is an image display method wherein the switching operation to the first display mode is performed if

the processing time is continuously less than the reference value during
predetermined number of frame in the case where the second display mode is set.

The eighth embodiment of the present invention is a game system wherein
the display mode setting device sets a switching to the first display mode when

5 the processing time is continuously less than the reference value during
predetermined number of frames in the case where the second display mode is set.

The ninth embodiment of the present invention is a computer readable
storage medium storing an image displaying program wherein the program is
formed so as to make the computer switch to the first display mode when the
10 processing time is continuously less than the reference value during
predetermined number of frames in the case where said second display mode is
set.

According to these embodiments, the switching operation is not
immediately conducted even when the processing load is lightened during the
15 setting to the second display mode, and the switching operation is conducted after
the consecutive situation where the processing load is light. Consequently, a
steady switching operation can be realized.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram showing brief constitution of the game system of one embodiment of the invention;

FIG. 2 is a diagram showing a data structure of the frame picture used in the first display mode; and

FIG. 3 is a flowchart showing a display processing in the embodiment of the invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

There will be explained hereinafter a preferred embodiment of the invention with referring to the drawings. Here, the embodiment in which the present invention is applied to a conventional home-use game system will be explained.

FIG. 1 is a block diagram showing a brief constitution of the game system of this embodiment. The game system shown in FIG.1 comprises a CPU 11, a ROM 12, a RAM 13, an interface 14, a controller 15, an external memory 16, a paint processing portion 17, a frame buffer 18, a monitor 19, a DVD-ROM drive 20, a sound processing portion 21 and a speaker 22. The system makes the

required game be operated by setting the DVD-ROM 10 storing a game program and game data on the DVD-ROM drive 20.

In FIG. 1, CPU 11 controls the operation of the whole game system and gives/receives control signals and data by being connected to each constitutional
5 element. The ROM 12 and the RAM 13 are used as storing devices. The operating system program and various kinds of data required for the operation control of the whole game system are stored in the ROM 12. A memory area is provided on the RAM 13 for tentatively storing game program readout from the DVD-ROM 10, or data necessary for the game proceeding.

10 The controller 15 connected via the interface 14 functions as an operation device for the player's operation during the game. The controller 15 comprises operation members such as a cross key 15a for inputting vertical and horizontal direction and a push button key 15b. Signals corresponding to the operation status of these operation members are output to the CPU 11. Further, external memory
15 16 is connected attachably to/ detachably from CPU 11 via an interface 14 and is used for rewritably storing the data which indicates the progress of the game. The player of the game can record a desired data on the external memory 16 by commanding it with the controller 15 as required.

The DVD-ROM 10 is a storage medium storing program for executing the
20 game and various kind of collateral data such as image data and sound data

relating to the game. The DVD-ROM drive 20 reads out required programs and data by conducting read-out motion on the inserted DVD-ROM 10 under the control of the CPU 11.

The paint processing portion 17 conducts painting process for image data which is read out from DVD-ROM 10 and processed by the CPU 11. As the display of the image data is made by each frame, the frame image data for each frame is generated at the paint processing portion. Frame image data is constituted by aggregation of the pixel data included in the image area of pixel number H for the horizontal direction and pixel number V for the vertical direction. The paint processing portion 17 performs display processing by generating 60 frame image data one by one per one second according to the NTSC system.

There are the first display mode and the second display mode as display processing of the game screen of this embodiment. As described later, switching operation to one display mode is conducted under the certain conditions by the control of the CPU 11. The first display mode is an arrangement where the pixel location is dislocated from each other by half-pixel size in the vertical direction between odd number frames and even number frames, and they correspond to different pictures respectively. Accordingly, the resolution can actually be doubled by the frame image data of the odd number frame and the even number

frame. On the other hand, in the second display mode, the pixel location of the odd number frame is same as that of the even number frame, and the resolution equals half of that of the first display mode.

Here, there will be explained a structure of a frame image data used in the first display mode by showing FIG. 2. In FIG. 2, a pixel arrangement on the monitor 19 corresponding to the frame image data for the odd number frame and the even number frame is respectively shown. In both of them, pixels number V in vertical direction and pixels number H in horizontal direction, $V \times H$ of pixel data p in total, are arranged. It is apparent that their pixel location is dislocated by half size of a pixel each other in vertical direction as indicated with the dotted line by comparing the odd number frame with the even number frame. Accordingly, in the case where the odd number frame and the even number frame are displayed alternately, pixel numbers in the vertical direction are increased to $2H$, resulting in the improvement of the resolution.

On the other hand, in the second display mode, the odd number frame and the even number frame are arranged at the same location with no difference in pixel arrangement of frame image data. Accordingly, the resolution of the image by the second display mode is actually half of that by the first display mode.

Then going back to FIG. 1, the frame buffer 18 stores 1 frame size of frame image data generated by paint processing portion 17. Accordingly, the paint

processing portion has a capacity for storing the frame image data comprising at least V x H of pixel data. The frame image data stored in the frame buffer 18 is read out by 60 frames per one second from frame buffer 18 and is output to the monitor 19. As the monitor 19, a home-use television set referenced to the NTSC system can be used.

The sound processing portion 21 converts the sound data read out from the DVD-ROM 10 into analogue sound signal and outputs it to the speaker 22. Further, the sound processing portion 21 generates sound effects and musical sound required during the game and converts them into analogue signal and then outputs it to the speaker 22 in compliance with the command made by the CPU 11.

Display processing in the embodiment of the present invention will now be explained by referring to FIG. 3. FIG. 3 is a flow chart showing a display processing for displaying a game screen on the monitor 19. In this embodiment, switching operation to/from the above described first display mode or the second display mode is made corresponding to the scale of the processing load controlled by the CPU 11. Here, the processing controlled by the CPU 11 performs each processing unit one by one as a loop so as to be synchronized with one frame. For example, each processing unit comprises a judgement routine of the content of the

operation by the controller 15, a consideration routine of the character, display control routine, and various kinds of other processing.

Firstly, in step S1, a required time for a unit processing within one frame is detected. If the processing performed in each frame includes a plurality of unit processing, the time required for each unit processing may be detected. For example, the time required for each unit processing can be detected by providing a clock device controlled by the CPU 11. Next, in step S2, the cumulative processing time T_a required for each unit processing is calculated. The cumulative processing time T_a may be directly detected by means of the clock device.

Next, in step S3, the cumulative processing time T_a within one frame is compared with the predetermined reference value T_r . This reference value may be set as approximately the same as a time for one frame. With the comparison result in step S3, if it is judged to be $T_a < T_r$ (step S3; YES), then the process proceeds to step S4. If it is not judged to be $T_a < T_r$ (step S3; NO), then the process proceeds to step S5.

In step S4, it is judged whether the above described first display mode is currently being set in display processing. In step S5, it is judged whether the above described second display mode is currently being set in display processing. With the judgement result in step S4, if it is judged that the first display mode is

currently set (step S4; YES), then the process proceeds to step S9. In a same manner, with the judgement result in step S5, if it is judged that the second display mode is currently set (step S5; YES), then the process also proceeds to step S9. Namely, both cases correspond to the case where the current display
5 mode is kept as it is.

On the other hand, with the judgement result in step S4, if it is judged that the first display mode is not set, but rather the second display mode is set (step S4; NO), then it is judged whether the situation of $T_a < T_r$ is consecutively made in M frames in step S6. The purpose of this process is to wait for the stability of the
10 status at a certain level even if the processing load is lightened in the setting process to the second display mode. In addition, the consecutive frame number M in step S6 may be set appropriately by considering the rapidness and the stability of switching.

If a continuous M frames of the condition $T_a < T_r$ is made as a result of the judgement in step S6 (step S6; YES), the process proceeds to step S9 after
15 switching to the first display mode in step S7. Namely, in this case, as it is judged that there is an enough margin in processing load, a priority is set on the picture quality and display processing with the frame image data in which pixel position of odd number frames is dislocated from that of even number frames as described
20 above is performed hereafter. On the other hand, if the number of frames in which

the condition $T_a < T_r$ is made is less than M as a result of judgement in S6 (step S6; NO), the process proceeds to step S9.

In addition, it is judged that not the second display mode, but the first display mode, is set as a result of step S5, (step S5; NO), the process proceeds to step S9 after switching to the second display mode in step S8. Namely, in this case, as it is judged that there is not enough of a margin in processing load, for lightening the processing load, the display processing with the frame image data in which the pixel positions of odd number frames and even number frames are arranged in the common position as described above is performed hereafter.

Next, in step S9, a paint processing for object frame is performed by the first display mode or the second display mode. Namely, the frame image data to be stored in frame buffer 18 is read out and output on the monitor 19 as a game picture of one frame size. Then, in step 10, it is judged whether the display processing of the game picture is finished. If it is judged that the processing is continued (step S10; NO), steps after S1 are repeated. If it is judged that the processing is finished (step S10; YES), the process in FIG. 3 is finalized.

As described above, in the process of FIG. 3, the switching from the first display mode to the second display mode or from the second display mode to the first display mode is performed. If the whole processing scale expands and it becomes impossible to catch up the paint processing, then the second display

mode which requires lighter load for paint processing is set. On the other hand, if the whole processing scale shrinks and there is an enough margin for paint processing, the first display mode which provides a better picture quality is applied. Accordingly, the disorder of the picture displayed on the monitor 19 can
5 be prevented beforehand and a high quality picture can be realized.

Although the invention is applied by example to the game system in the above described embodiment, this invention is not limited to the embodiment. The invention also can be applied to other forms of systems with image display processing. In addition, as a storage medium for storing game program which
10 makes the invention function, the CD-ROM, the floppy disc, the hard disc, and various kind of storage medium can be used in addition to the DVD-ROM.

As described above, according to this invention, a high quality image display is realized without the disorder on the screen even if the processing load increases during the image displaying.

15 The entire disclosure of Japanese Patent Application NO 2000-130254 filed on April 28, 2000 including specification, claims, drawings and summary are incorporated herein by reference in its entirety. While the invention has been described in connection with what is presently considered to be the most practical and preferred embodiment, it is to be understood that the invention is not to be
20 limited to the disclosed embodiment, but on the contrary, is intended to cover

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various modifications and equivalent arrangements included within the spirit and scope of the appended claims.